

Fig. 1

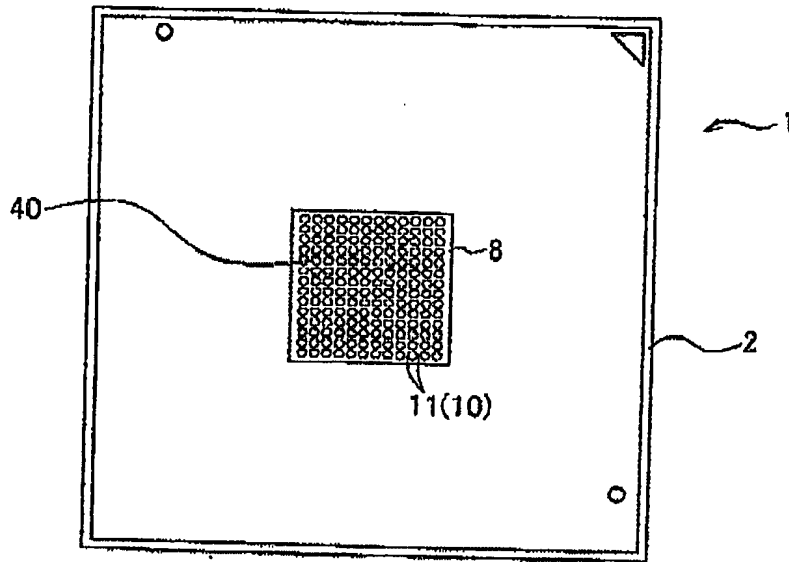
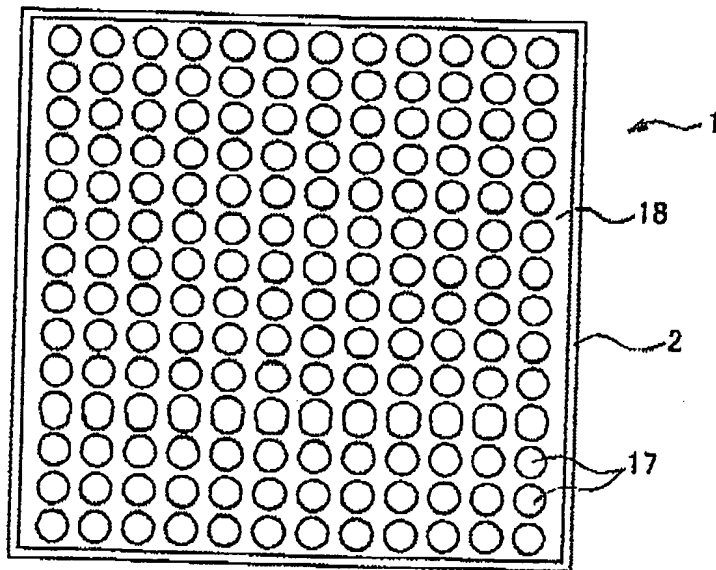


Fig. 2



The diagram shows a cross-section of a semiconductor device with two main functional layers, L1 and L2. Layer L1 is positioned above layer L2. Various conductive and insulating regions are labeled with numbers and letters, indicating different materials or structures used in the device's construction.

Fig. 5

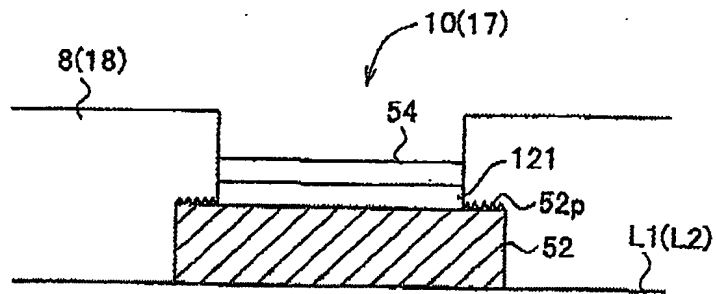


Fig. 6

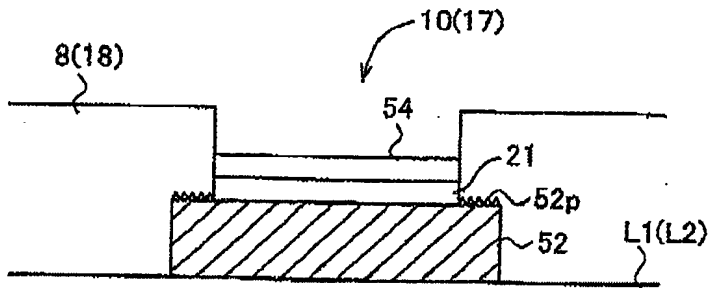


Fig. 7

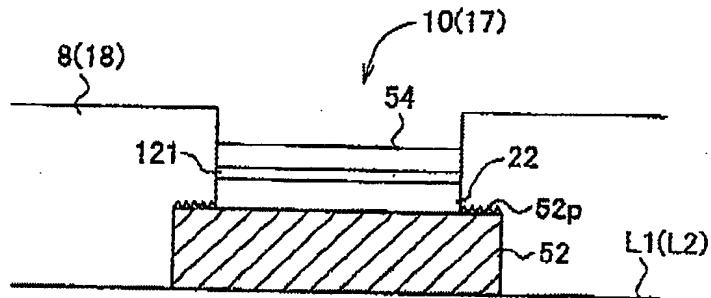


Fig. 8

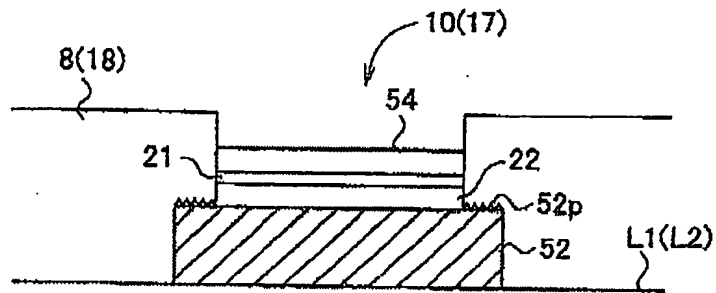


Fig. 9

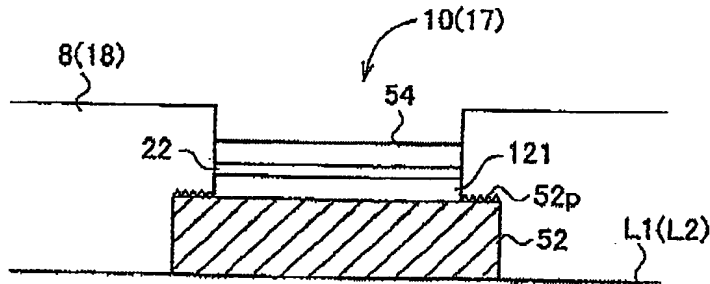
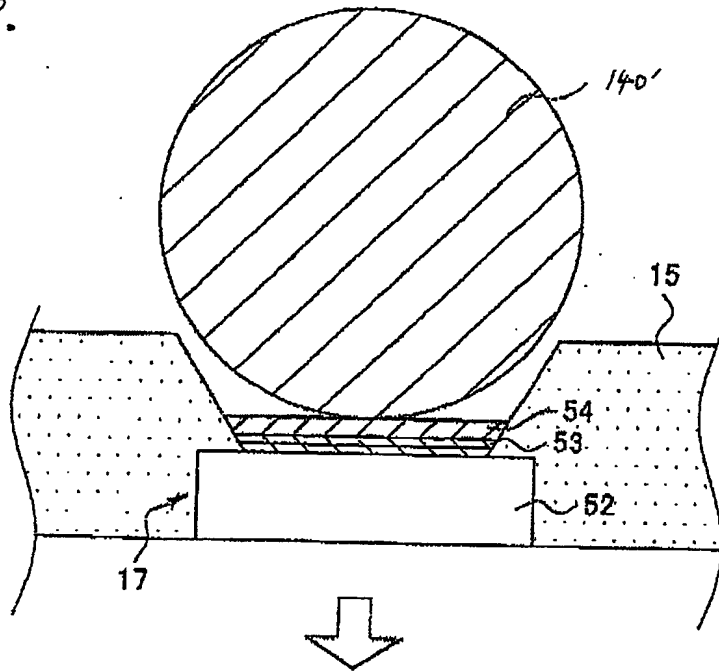
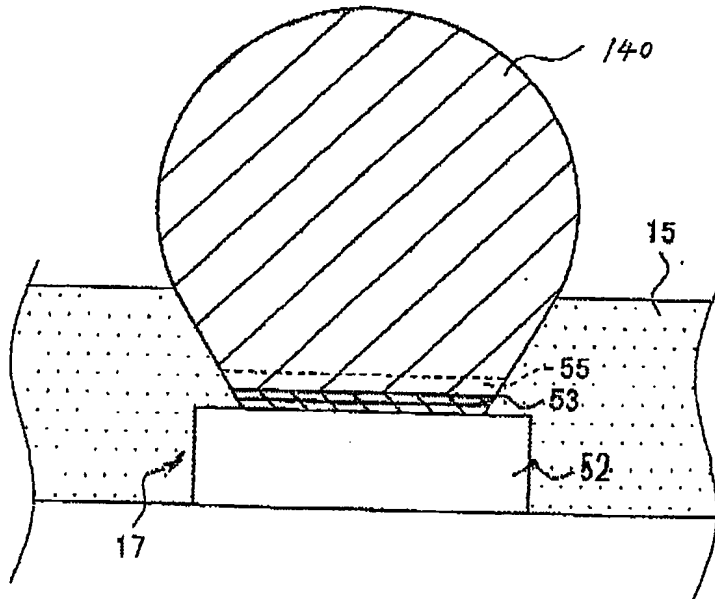


Fig. 10.



STEP 3



STEP 4

Fig. 11

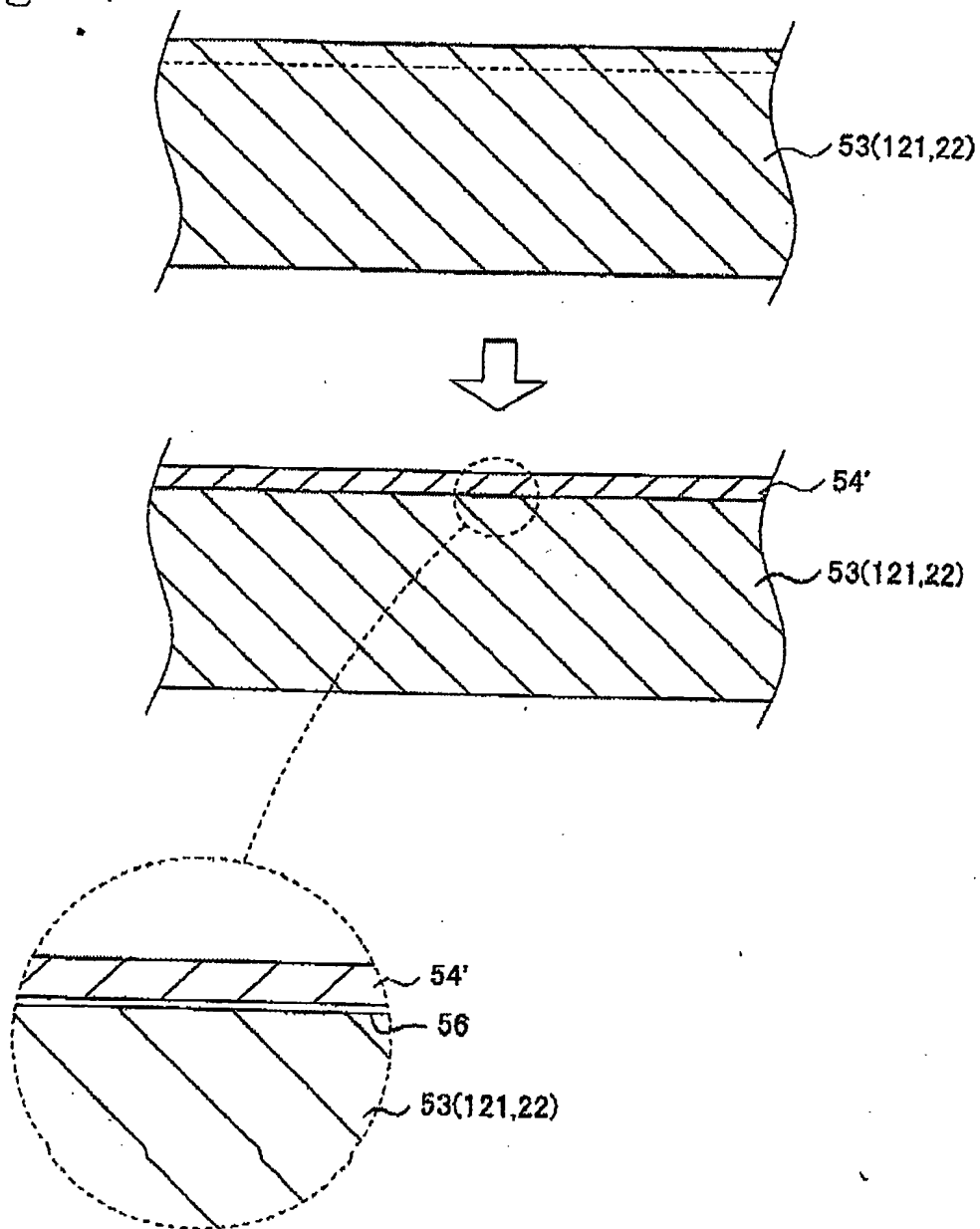


Fig. 12

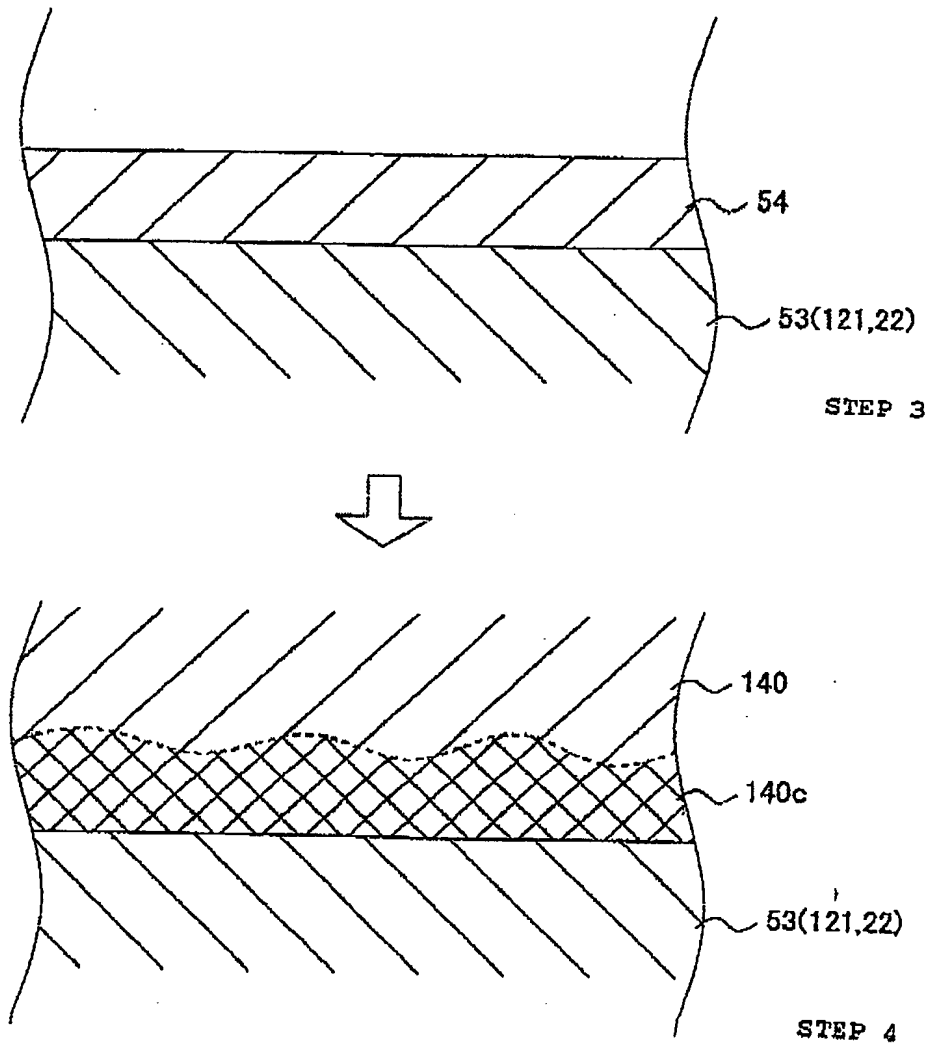


Fig. 13

